

Vivekanand Education Society's Institute of Technology

Hashu Advani Memorial Complex, Collector colony, Chembur-400 074

(ON BEHALF OF UNIVERSITY OF MUMBAI)

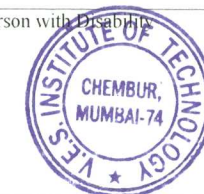
College Code 366

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THIRD YEAR ELECTRONICS ENGINEERING SEMESTER - V I [C - 2019] SUMMER - 2025 (CBCGS) Examination held in MAY - 2025

Sr. No.	Name of the Student Seat No.	ELC601 Basic Vlsi Design			ELC602 Electromagnetic Engineering			ELC603 Computer Communication Networks			ELC604 Embedded Systems And Real Time Operating Systems			ELDO603 Machine Learning			ELL601 Basic Vlsi Design Lab			ELL602 Computer Communication Networks Lab			ELL603 Embedded Systems And Real Time Operating Systems Lab			ELL604 Database Management Systems Lab			ELM601 Mini Project-2 B			Outof 750	Result		
		SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	EC	ECG	GPA	
1	ADSULE CHANDAN RAMESH NETRA EX6X001	21F	09+	30	32+	13+	45	41+	13+	54	38+	11+	49	32+	13+	45	14+	13+	27	20+	19+	39	17+	15+	32	41+	21+	22+	43	405					
		Grade	F	E	--	P	C	E	D	C	D	E	D	E	P	C	E	D	D	D	O	A	A	C	C	C	O	O	O	F					
		Grade Pt.			--			5			6			5			5			6			9			7			10			10			
		Credit Pt.			--			3			3			3			3			1			1			1			2			2			
		CP*GP			--			15			18			15			15			6			9			7			20			20			



/:FEMALE, #:0.229, @:0.5042, *:0.5045, ADC - Admission Cancelled, RR-Reserved, -- : Fails in Theory or Practical, PRV - Provisional, RCC - 0.5050, A.ABS - Absent, F - Fails, P - Passes, NULL-Null & Void, ~ Dyslexia Benefit/Person with Disability
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 [Marks/Grade/GradePoint] : [>=80 and <=100 / O / 10] [>=75 and <80 / A / 9] [>=70 and <75 / B / 8] [>=60 and <70 / C / 7] [>=50 and <60 / D / 6] [>=45 and <50 / E / 5] [>=40 and <45 / P / 4] [>=0 and <40 / F / 0]

Entered by : *KB*

Read by : *[Signature]*

Checked by : *[Signature]*

Principal : *[Signature]*

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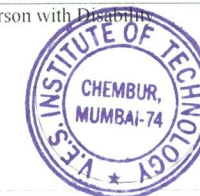
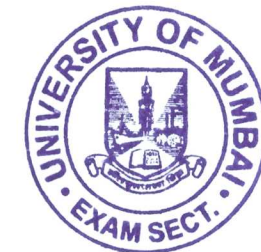
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		SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	EC	ECG	GPA
1	NIKHARE SHRUTI GAJANAN LAXMI EX6X002	34	12+	46	32+	10+	42	37+	12+	49	32+	11+	43	32+	12+	44	13+	11+	24	22+	19+	41	14+	17+	31	43+	21+	22+	43	406				
		Grade	P	C	E	P	D	P	E	C	E	P	D	P	P	C	P	D	P	E	O	A	O	D	C	C	O	O	O	P				
		Grade Pt.			5			4			5			4			4			5			10			7	10			10				
		Credit Pt.			3			3			3			3			3			1			1			1	2			2	22	128	5.82	
		CP*GP			15			12			15			12			12			5			10			7	20			20				



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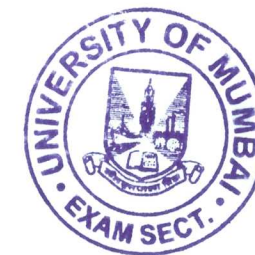
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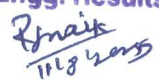
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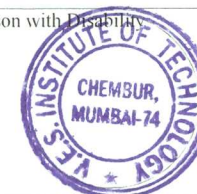
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Sr. No.	Name of the Student Seat No.	ELC601 Basic Vlsi Design			ELC602 Electromagnetic Engineering			ELC603 Computer Communication Networks			ELC604 Embedded Systems And Real Time Operating Systems			ELDO604 Digital Design With Reconfigurable Architecture			ELL601 Basic Vlsi Design Lab			ELL602 Computer Communication Networks Lab			ELL603 Embedded Systems And Real Time Operating Systems Lab			ELL604 Database Management Systems Lab			ELM601 Mini Project-2 B			Outof 750			
		SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	SE	IA	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	TW	P/O	TOT	EC	ECG	GPA	
1	/SHINDE SONAL SANJAY SANJANA EX6X003	Marks Obtained	A	08+	8	A	A	0	00F	10+	10	02F	12+	14	A	11+	11	10+	10+	20	22+	15+	37	12+	15+	27		22+		12+	15+	27			176
		Grade	F	P	--	F	F	--	F	D	--	F	C	--	F	D	--	P	P	P	O	C	B	E	C	D		P		E	C	D			F
		Grade Pt.			--			--			--			--			--			4			8			6		4				6			
		Credit Pt.			--			--			--			--			--			1			1			1		2				2	7	38	--
		CP*GP			--			--			--			--			--			4			8			6		8				12			
2	/VEER SHREYUSHI ASHOK NETRA EX6X004	Marks Obtained	A	04F	4	32+	08+	40	50+	13+	63	53+	08+	61	42+	08E	50	10+	10+	20	14+	16+	30	12+	13+	25		35+		13+	15+	28			356
		Grade	F	F	--	P	P	P	C	C	C	C	P	C	D	P	D	P	P	P	D	C	C	E	D	D		B		D	C	D			F
		Grade Pt.			--			4			7			7			6			4			7			6		8				6			
		Credit Pt.			--			3			3			3			3			1			1			1		2				2	19	117	--
		CP*GP			--			12			21			21			18			4			7		16				12						





Superintendent
 (Engg. Results Unit)




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Entered by : 

Read by : 

Checked by : 

Principal : 