



# Vivekanand Education Society's Institute of Technology



*Affiliated to University of Mumbai & Approved by A.I.C.T.E.*

*“Arise , Awake and Stop not till the Goal is reached”.*  
*- Swami Vivekanand*

## Prospectus 2022-23



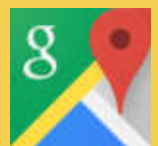
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*Social Media Connect:*





## About V.E.S.

Vivekanand Education Society(V.E.S.) was founded in 1959 by Shri. Hashu Advani, along with ten other members, had a dream of providing qualitative education to the youth of our country. In the beginning, Vivekanand Education Society had a very modest launch, with just 256 students and six classrooms, in the humble barracks of Chembur Camp. But today, it proudly boasts of having 3,75,000 sq. ft. land, housing 12 buildings and 28 institutions, ranging from a crèche to Ph.D. Centers. It has over 2000 teaching and non-teaching staff, and more than 18,000 students who pass through its hallowed portals each year. VES strictly follows and strongly believes in – **No management quota, no capitation fees, or donations in any form.** The institution lays emphasis on the building of character and personality of students.

Vivekanand Education Society's Institute of Technology (VESIT) was established in 1984, with the aim of providing professional education in the field of Engineering. VESIT is a private unaided engineering college run by the Vivekanand Education Society (VES) trust. This institute is affiliated with the University of Mumbai and follows the rules and regulations laid down by the Government, AICTE, and University of Mumbai for admission; **51% are reserved for Sindhi Linguistic minority.** The management quota has been surrendered to DTE to make admission centralized. The institute has highly qualified faculty who are selected on the basis of their accomplishments.

**VES Institute of Technology** in short “VESIT” is making its felt in Maharashtra by being on top in the preference of taking admissions by the students, because of its efficient functioning on imparting quality education through a process of innovative teaching inherited with techniques like process oriented guided inquiry learning, peer learning, flipped classroom and Google classroom concept.

# 28 Institutes of V.E.S.

58 YEARS  
OF EXCELLENCE

VES Creche  
and  
Day Care Centre

VES Business School

Swami Vivekanand  
English Primary School

VES Institute of  
Management Studies &  
Research

VES DBC  
Pre- Primary  
School

Swami  
Vivekanand English  
Pre-Primary School

VES  
Tulsi Technical Institute

Swami Vivekanand  
Prathamik Shala  
(UNAIDED)

VES  
DBC Primary School

VES College of Arts,  
Science & Commerce  
(Aided - Courses)

Swami Playgroup  
& Nursery

VES College of  
Pharmacy

Swami Vivekanand  
Kanishtha  
Mahavidyalay (UNAIDED)

VES Institute of  
Technology

Swami Vivekanand  
Prathamik Shala  
(AIDED)

VES College of Arts,  
Science & Commerce  
(Self- Financing Courses)

Swami Vivekanand  
Kanishtha  
Mahavidyalay (AIDED)

VES College of Law

Swami Vivekanand  
Purva-Prathamik Shala

VES Polytechnic

Swami Vivekanand  
High School

Swami Vivekanand  
Junior College

VES College of  
Architecture

Vivekanand Education  
Society (H.O.)



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# About US

- Vivekanand Education Society (VES) was established in 1959 under the able leadership of Late Shri. Hashu Advani, Former Finance Minister Govt. of Maharashtra.
- It proudly boasts of having 3, 75, 000 sq. ft. land, housing 12 buildings and 28 institutions, ranging from crèche to Ph.D. Center.
- It has over 2000 teaching and non-teaching staff, and more than 18,000 students.
- VES strictly follows and strongly believes in No Management Quota, no capitation fees or donations in any form.
- Self Financed Linguistic Minority Institute with 51% reservation for Sindhi Linguistic minority.
- Approved by the AICTE, State Government and is affiliated to the University of Mumbai.

## Vision

“To create a vibrant knowledge - oriented environment with innovative teaching practices and to inculcate tradition of socially conscious application of technology.”

## Mission

- To inculcate culture of value based education.
- To enthuse students to develop in an ambient environment of caring and of sharing information.
- To enable students to work towards excellence in their chosen fields with professional bent of mind.



## FROM PRINCIPAL'S DESK

We are extremely happy to have our students back in the campus after two years and glad to see amphitheater bustling with student activities. All said and done, the last two years of disruption was a period of transformation for everyone, be it businesses, academic institutions, government departments and individuals.

Let us hope that the awareness of uncertainty knocking at our doors any time, has made us better human beings and value what we have in hand, while we focus on improving the existing condition by finding solutions to the problems.



During this period VESIT also was preparing to apply for NAAC accreditation which is a first step towards autonomy. As of now, we have completed the application process and are awaiting the Peer Assessment Team to visit the institute. We are also happy to report that our institute has been awarded by the National Education Excellence Awards Team as the most 'Innovating Engineering & Technology' of the year 2021 and also recognized our efforts in "Outstanding performance in Virtual Knowledge delivery during pandemic".

The National Institute Ranking Framework (NIRF) has categorized our institute in the band of 251-300 from thousand elite technical institutions across the country. The ranking is based on varying parameters ranging from Research to Perception.

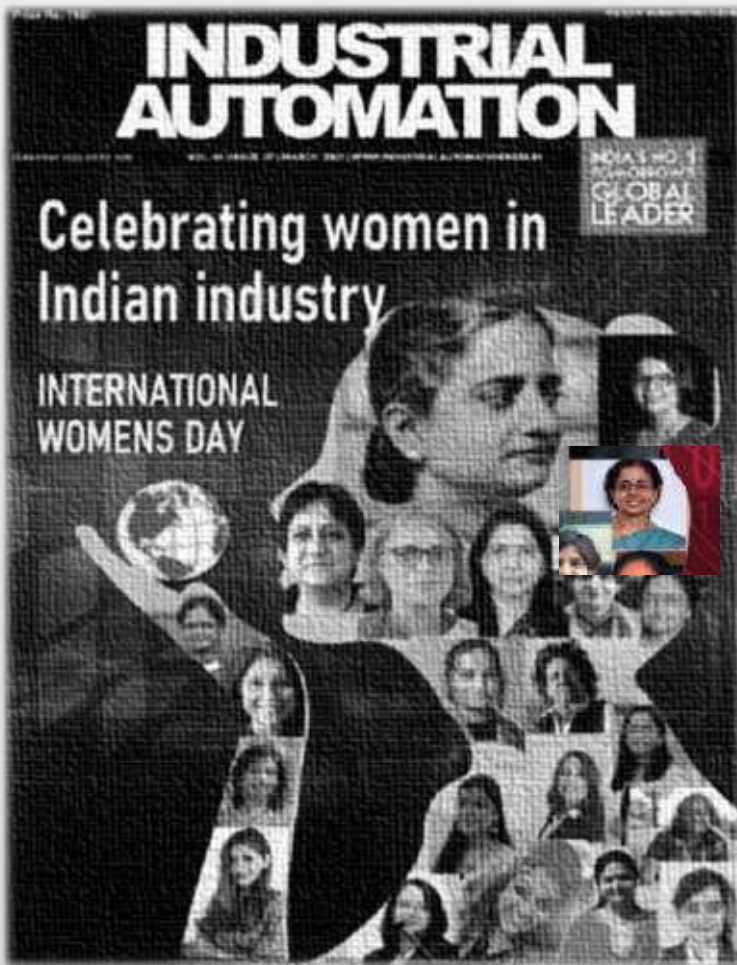
Another proud moment for VESIT is when three of our research projects were selected to the semi-final round in the Swadeshi Microprocessor Challenge (SMC). SMC is an initiative of the Ministry of Electronics and Information Technology in collaboration with IIT Madras and CDAC to provide innovative solutions for Atmanirbhar Bharat. SMC was launched to provide innovative frugal solutions in public utility services and in strategic areas of space, Defence and Nuclear energy, using the industry grade microprocessors designed by IIT Madras (Shakti Processor) and CDAC (Vega Processors), powered by FPGA boards XiLinX. The fact that VESIT is the only educational institute to bag three projects among 100 semi-finalists which were, shortlisted from 6200 entries is worth mentioning. Each semi-finalists team got free access to Swadeshi Microprocessor and FPGA boards and development funding of Rs. One Lakh to develop a Minimum Viable Plan (MVP) / POC (Proof of Concept). One of these projects "Calibration systems for Nuclear Spectroscopy Applications" received a grant of Rs. 25 Lakhs and secured fourth place in the finals. The project was developed by Mr. Ajit Patil, Post Graduate student of Instrumentation Dept. and Mrs. Asma Parveen, Faculty of Information Technology and was mentored by Dr. P. P. Vaidya, Dean of R&D, VESIT. We are delighted to inform you that VESIT is the only educational Institute from Maharashtra in the list of winning Institutes.

We congratulate Mr. Ajit Patil of M.E Instrumentation and Control on receiving "Runner up Student Research Forum Award of Rs.25000/-" for Design and construction of Precision and Sliding Pulse Generator in "The International Conference on VLSI Design and Embedded Systems (VLSID2022)", which was successfully held in virtual mode from 26-Feb to 2-Mar, 2022. Our institute participated in CiiA Inter Institution student innovations Exhibition & Competition which was held on 29 & 30 March 2022 at Mumbai. We had 15 major exhibits, which was the highest among the Engineering and Technical colleges category, and was selected by CIIA screening & Selection Committee for the same. Recognizing the efforts of our Institute Management, Faculty and staff for putting the Innovation Ecosystem in place, the CiiA awarded us the Max Trophy which was handed over by Vice Chancellor of Mumbai University Mr. Suhas Pednekar and Dr. Prabhat Ranjan, Vice Chancellor, D.Y. Patil International University.

VESIT was also entitled to a free space at their exhibition for branding and showcasing our infrastructure, the ecosystem and support provided to our students to undertake innovations & research projects.

The Project titled “Prime Era – One Place for Learning Fundamentals of Regional Languages” from Computer Dept. Mentored by Mrs. Indu Dokare won first prize at VISAI 2022 International Project Competition – A Virtual 12th International Project Competition and Exhibition based on Sustainable Development Goals organized by Vel Tech University. To instill in our students the culture of finding viable technological solutions to practical problems, we encourage them to participate in Hackathons. The internal Smart India Hackathon, Hackathons conducted by Computer Dept. and IT Dept. also gave students an exposure in this direction.

To guide our budding entrepreneurs, a three days “Entrepreneurship Awareness Camp” was organized by Institute Innovation Council of VESIT in association with Maharashtra Centre for Entrepreneurship Development Institute of India (EDII), Ahmedabad, sponsored by Dept. of Science & Technology, Govt. of India during third week of March 2022. The two day lecture was followed by an Industrial visit to Panvel Co-operative Industrial Estate.



VESIT believes in the power of collaborative research and provide opportunities for our students to work in the niche areas mentored jointly by our faculty with expert faculties from other institutions. Few of the collaborative projects are: projects by the Computer Dept.co guided by University of Wisconsin-Madison Faculty; project “e-Ganit” from I.T. Dept. with faculty from College of Engineering, Pune; project “EMG based Gesture Recognition using Armband” by Instrumentation Dept. with Bio-Science Dept. of IIT, Bombay and Computer Department projects with Tata Institute of Fundamental Research and University of Texas.

VESIT celebrated April 8, 2022 as VESIT –TIFR day and Dr. Shashikant Dugad, Professor, TIFR delivered an expert talk on ‘Technology Frontiers on High Energy Physics’, which was

followed by presentations of the project work carried out by Computer students in collaboration with TIFR and University of Texas.

We are very happy to report that project developed by one of the groups in “Automated visual inspection of silicon detectors in the CMS experiment “at CERN, Geneva is going to be deployed in the actual system.

Dr. Dugad also showed interest in the electronic projects developed at the institute and we are now exploring the possibilities of collaborative research in this field too.

New Education Policy (NEP) 2020 envisages academic flexibility and inter disciplinary projects. Keeping this in mind, we encourage our students to participate in competitions from diverse fields.

In this endeavor, our students from Instrumentation Dept. participated in “Solar Decathlon” along with students from Architecture. Two of our teams were selected to the final round in the category of ‘Education Building Development’: Team ‘Abhijan’ where our students had partnered with VES College of Architecture and proposed an energy efficient design for VES Jr. College building and Team ‘Venturis’ which had partnered with NMIMS’ Balwant Sheth School of Architecture. As another initiative in this direction, VESIT started an Outreach Centre with Indian Institute of Remote Sensing (IIRS) under ISRO. Here, regular courses are conducted on advanced fields of remote sensing and Geo information science.

This upcoming field is a backbone in many decision making systems and location based services. Geo spatial technologies can be used for variety of applications in environmental analysis, forestry, mining, water resources as well as the infrastructure.

We are also proud of our Alumni Mr. Suraj Nair (Computer Engg. Batch 2010) a Squadron Leader for being conferred the coveted “Vayu Sena Award” on 26th January 2022 for his bravery and Mr. Anup Mhatre (Elec. & Telecomm. Engg. Batch 1989-99) for taking charge as Director, Planning & Strategy Chapter Management & Operations of MSME & Startups Forum-Bharat. VESIT Renaissance Cell (VRC) in association with VESIT IIC and IQAC had organized the technical festival of VESIT, Praxis ‘22. Praxis was held on 31st March 2022, with the Pre-Praxis events conducted from 19th March 2022 to 30th March 2022. All Technical Societies within VESIT viz. IEEE-VESIT, ISTE-VESIT, CSI-VESIT and ISA-VESIT and E-cell had organized one Pre-Praxis event and one Praxis event. VRC had organized two Pre-Praxis events and one Praxis event. In all, thirteen technical events were organized with approximately 1500 students participating in it.

As our country celebrates the spirit of Azadi ka Amrut Mahotsav, we at VESIT have scheduled a Technical Conclave “Tech4Good” on April 22 & 23, 2022 in association with the Directorate of Technical Education, Maharashtra State. The Online Technical conclave will comprise of Invited talks and Hands-on workshop in emerging areas of Technology and a competition for projects depicting conscious applications of technology in tune with our vision.

The keynote speaker for the event is Dr. Shasikumar, Senior Director, CDAC. A panel discussion on ‘Emerging technologies for Atmanirbhar Bharat’ with Mr. Narendra Shukla, Dark Horse Digital, Mr. Devesh R., Founder, Cerelabs, Mr. Amit Rambia, Chairman and MD-Panache Digilife Ltd and Dr. Deepak Garg, Dean, CSE Dept, Bennet University as distinguished panel members has also been planned. The project competitions and Hands on Workshops on Hardware and Software tracks will be conducted in parallel sessions. VESIT as a part of Unnat Bharat Abhiyan was granted Rs. 1,00,000/- by MHRD out of which, Rs. 50,000 was sanctioned for survey of 5 villages (Ambiste kh., Amgaon, Aпти, Asnas and Gargaon) in Maharashtra and Rs. 50,000/- for Surya Skshamata proposal in the village of Ambiste Kh, Tahsil- Wada, Dist- Palghar, Maharashtra, India.

Our students also excel in extracurricular activities. Mr. Venkatesh Arumugam, student from MCA Dept. was selected to represent the Mumbai University at All India Inter-University Fencing (Men & Women) Competition 2021-22 and Ms. Janhavi Khanvilkar, a second year student of AI & DS Branch was

selected to represent Mumbai University at All India Inter-University Rifle/ Pistol (Men & Women) Shooting Championship 2021-22 which was held at Manav Rachana International Institute of Research & Studies from 31st March to 3rd April, 2022.

We started with off line classes in March and students, teachers and exam dept are now gearing up to conduct the first offline test in April, after a gap two years. We congratulate the vibrant Student Council team, who managed to conduct all the sports and cultural events of the year in a short span of time.

*Have faith that you are all, my brave lads, born to do great things! Let not the barks of puppies frighten you -- no, not even the thunderbolts of heaven – but stand up and work! - Swami Vivekanand*

**Dr. J. M. Nair**

**Principal**

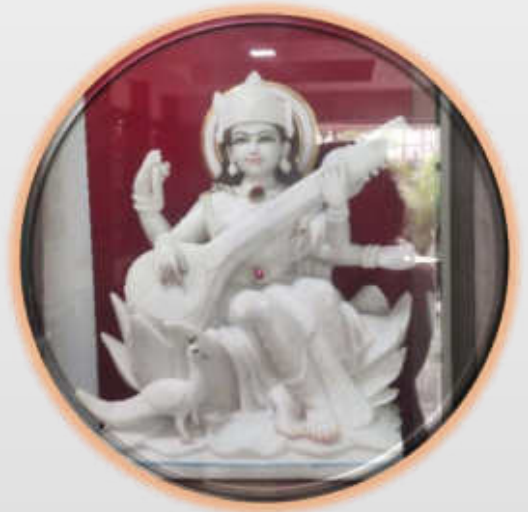
## FROM VICE PRINCIPAL'S DESK



VESIT takes pride in being able to impart holistic education to all its students. Our vision indicates our dream of being able: To create a vibrant knowledge-oriented environment with innovative teaching practices and to inculcate a tradition of socially conscious application of technology.

As the world crawls back to normalcy after two years of pandemic disruption, it gives me great pleasure to welcome our students back to the campus.

Even as Digital learning and E-Events took to being the new normal, it is indeed heartening to see a return of physical festivities to the colleges. A holistic education is possible only when students are able to interact with each other and the environment; physically, rather than just virtually. But this disruption has brought a new mode of hybrid education into the field of education. Inevitably, both physical and virtual classes and events will live side by side. At VESIT we are proud to say that we have wholeheartedly embraced this confluence of physical and digital environment.



Receiving the award for having the most number of innovative projects at the first CIIA Mega Exhibition for Innovative Projects gives us a great sense of achievement.

We are pleased to present the first National Level Technical Conclave ”**TECH4GOOD-2022**, an All Project Competition, Invited Talks, and Workshops collaborated with DTE Maharashtra, which will be held 22-23 April 2022.

These are indeed exciting times to look forward. I am reminded of the saying by Arthur C. Clarke, *The only way to discover the limits of the possible is to go beyond them into the impossible.*

This has never been more true than the present time.

All the very best.

**Dr. M. Vijayalakshmi**  
**Vice Principal, VESIT**



 **Institution Innovation Council (IIC)**

 **Ph.D. Centre of University of Mumbai  
(Electronics Engineering)**

 **R & D Centre**

 **Permanent Affiliation to University of  
Mumbai**


 **Competency Development Centre of  
Automation Industries Association (AIA)**

 **LabVIEW Academy with National  
Instruments**


 **Training provided for Industries in the State  
of the Art Instrumentation Laboratory**

 **Matlab Campus Wide License**


 **Innovation Lab with Accenture and D- Link  
Academy**

 **Zonal Partner with leadingindia.ai**

# VESIT SALIENT FEATURES


 **Active Placement Cell**

 **Highly Qualified Teaching Faculty**

 **Wi-Fi Campus with 100 Mbps Bandwidth**

 **Aditya Jyot Eye Innovation Center**


 **E Yantra Lab with IIT**

 **Oracle Academy**

 **Professional Societies of Students**

 **Big Data Analytics Lab with Cloudera, USA**

 **Sprawling Complex, Inviting Playground &  
Hostel**

 **Library has more than 49596 books**

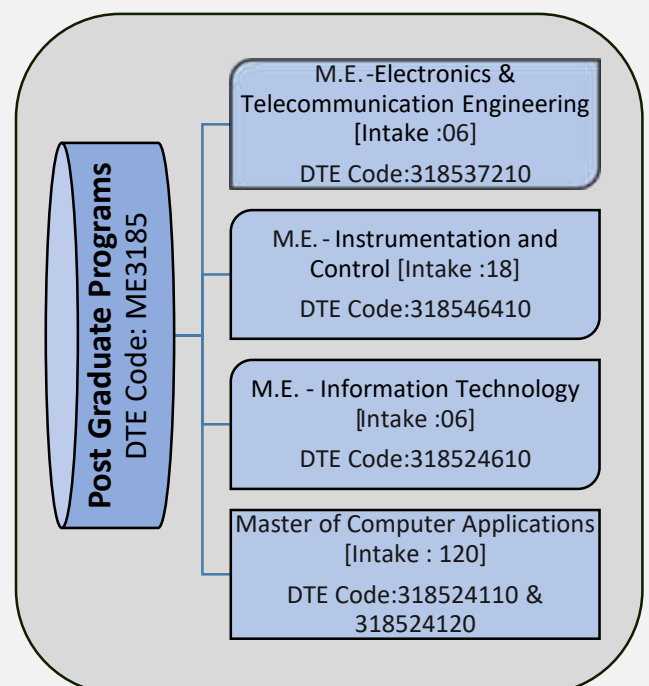
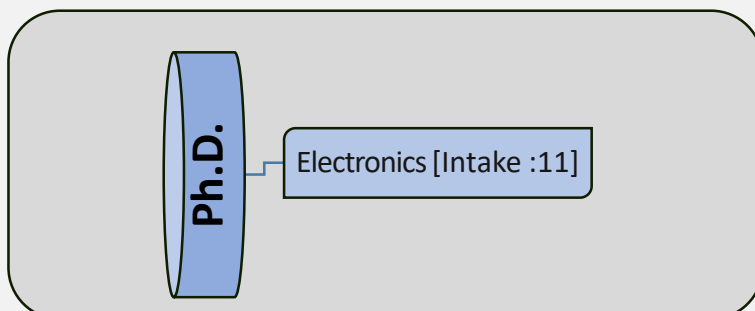
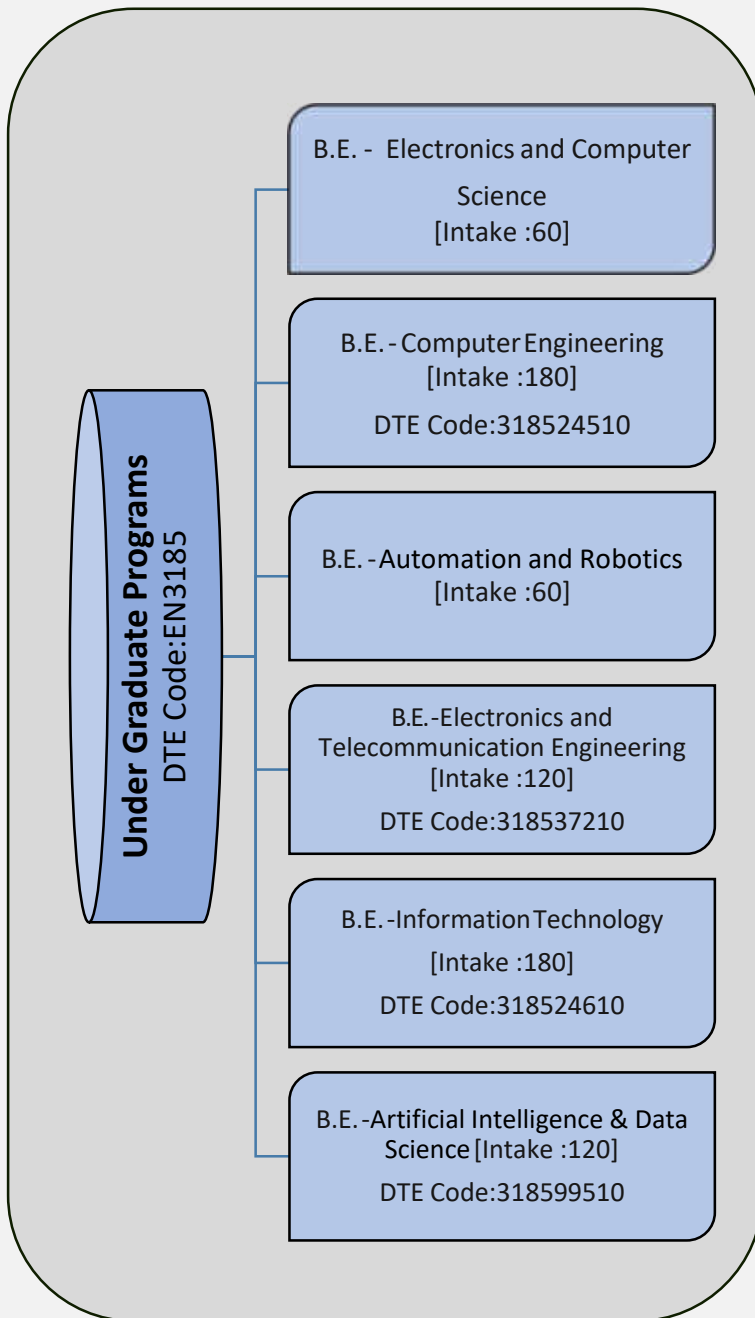


# Courses offered

Institute DTE Code: 3185



Private Unaided Sindhi Linguistic minority Institute with 51% quota for minority



# VESIT Ranking



Two times accredited by National Board of Accreditation in 10 Years for all eligible UG Courses



NIRF India Rankings 2021 in the RANK-BAND of 251-300 (Engineering)



Ranked In The Band Of 26th - 50th In ARIIA 2019 Under Self-finance/ Private Institutions Category.



1st RANK in Maharashtra State in Education World Private Engineering Institutes Rankings in 2022-23, 2020, 2019, 2018



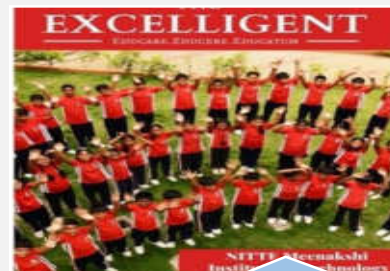
"India's Best Engineering Colleges 2019" by Career 360



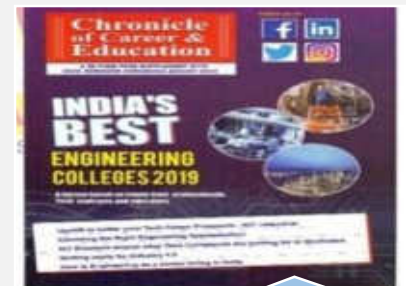
Asia's Fastest Growing Institutes by WCRC, Award ceremony held at Singapore.



"Times Engineering Institute Ranking Survey 2019" by Times Of India



"India's 10 Best Institutes for Higher Education 2019" by The Excelligent Magazine survey.



Ranked in Surveys "India's Best Engineering Colleges 2019" by Chronicle of Career and Education.



The Week-Hansa Research Survey 2020 "Best Colleges 2019" by The WEEK



"Best Colleges of India" by The India Today



"The 10 Best Private Institutes in India, 2018" by The Knowledge Review Magazine

## Placement Information

The legacy of excellent placement records continues-

### Highlights of 2021-22

- ❖ 4th Consecutive year with 600+ placement offers.
- ❖ 766+ Placement offers till date for 2021-22 batch.
- ❖ Highest package of CTC 29.25 LPA for 2021-22 batch.
- ❖ 94+ recruiters visited till date for 2021-22 batch.
- ❖ Approx. 50% of Recruiters/Companies are DREAM companies.

### Placement Details of 2021-22

- ❖ No of placement offers: 766\*
- ❖ Number of Students placed: 618 out of 815 students
- ❖ Number of students having multiple job offers: 148
- ❖ % Offered( Registered): 93.98
- ❖ % Placed(Registered): 75.82

### 2022-23 Batch

- ❖ Total offers(till date)-\*329
- ❖ Highest CTC- 22 LPA
- ❖ Lowest CTC- 8.5 LPA
- ❖ Total Companies Visited-29
- ❖ Total Placed - 319

\*(As on 24th Sep. 2022)

#### \*Maximum salary offered: INR 29.25 Lakhs/Annum

One candidate from B.E. Computer Engineering offered with Internship opportunity by Commvault which will lead to performance based conversion. Once converted the candidate will eligible for the package of Rs. 29,25,000/- (Twenty Nine Lakh Twenty Five Thousand only) for the A.Y 2021-22 batch.

# Placement Information



The legacy of excellent placement records continues-

## Major Recruiters



## Dream Recruiters



# INFRASTRUCTURE

The VESIT campus strikes a balance between modern facilities and natural surroundings to create an atmosphere that is ideal for students to learn, explore, and grow.

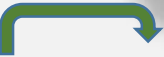
**Smart Class Rooms**



**Amphi Theatre**



**Equipped Library**  
49596 books, 30 Journals



**Seminar Hall**



**Huge Playground**



**Canteen**



**Music Room**



# Laboratories with Latest Technology



**Robotics & AI**



**Software defined Radio  
(Under AICTE MODROB grant)**



**Web Engineering and Multimedia  
(Under AICTE MODROB grant)**



**Cloud Computing**



**Machine Learning and Data Analytics**



**Process Automation**



**RF, Microwave & Antenna Comm.**



**DSP & IP open source**



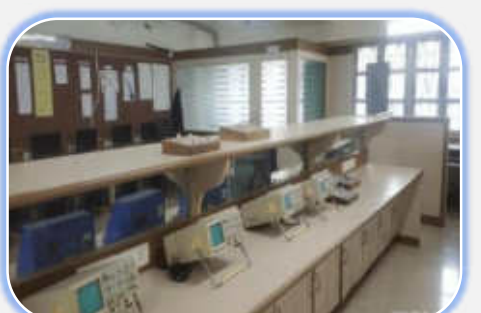
**D-Link Academy, Network &  
Wireless Comm.**



**TV, VLSI & Satellite Comm.**



**Optical Fiber Comm & Network**



**Basic Electronics**



**Big Data Analytics**



**PLC-DCS**



**E-Yantra**

## Achievements

### Tech4Good

A National Level Online Project Competition  
Step towards Aatma Nirbhay Bharat



Directorate of Technical Education (DTE) Maharashtra State  
And  
Joint Director of Technical Education, Regional Office, Mumbai  
In collaboration with  
Vivekanand Education Society's Institute of Technology (VESIT)  
and in association with  
Dark Horse Digital

75  
Azadi Ka  
Amrit Mahotsav

Presents  
**Tech4Good-2022**  
A National Level Technical Conclave

Theme: Advanced Technological Innovations for Societal Good

Project Competition  
Invited Talk  
Workshop

Prizes worth of ₹1 lakh+  
UG Level  
PG/Ph.D. Level

### Creative Ideas and Innovations in Action (CIIA-2022)



- VESIT participated in CIIA Inter Institution student innovations Exhibition & Competition held on 29th & 30th March 2022 in Mumbai
- CIIA screening & Selection Committee selected 14 major exhibits - highest among the Engineering and Technical colleges category



**Maxtrophy Awarded for maximum selection – 14  
Projects, 16 leads from visitors of exhibition**

## Achievements

### PROJECT SWECHCHHA



**“An ITeS based Solution for Operation & Maintenance of the Community and Public Toilets”  
Received Approval from MCGM on 20-08-2020 for 2 years (20-08-20 to 20-08-22)**

The CSR- Financial Partner is **Star Union Dai-ichi Life Insurance Co. Ltd. (SUD Life)** who proposed to provide financial assistance of **Rs. 24 lakhs** for the project **VESIT-IT Swechchha.**

### Members of SUD life foundation

**Mr Gnana William:** Chief Internal Auditor of SUD, Trustee of SUD Life Foundation

**Mr Abhay Tiwari:** Chief Actuary and Joint President of SUD Life

**Mr Rakesh Kumar:** Company Secretary of SUD and Managing Trustee of SUD Life

**Mr Rajesh Mourya:** Manager, CSR, SUD Life

**Mr Anand Jagtap,** Ex MCGM - OSD Slum Sanitation Program

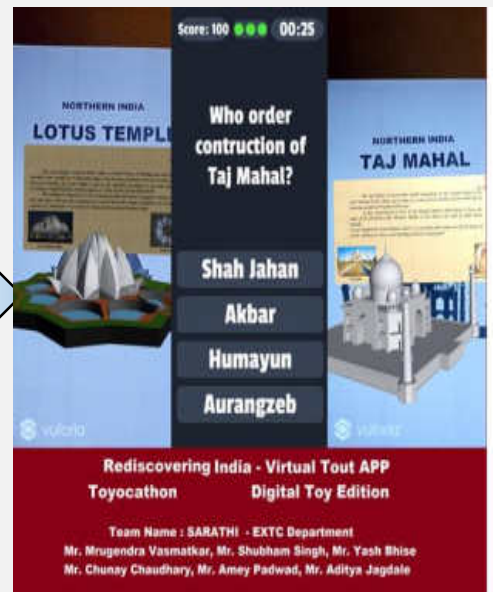
## Achievements

### TOYCATHON 21'

Inter-ministerial initiative by Ministry of Education's Innovation Cell with support from AICTE, Ministry of Woman and Child Development, Ministry of Commerce and Industry, Ministry of MSME, Ministry of Textiles and Ministry of Information and Broadcasting.



04 Ideas from  
VESIT  
Shortlisted  
for the  
Grand Finale



**“Junior’s Farm”  
Hardware Toyacathon  
(Finalist)**



**“Rediscovering India Virtual  
Tour App”  
Digital Toy Edition(Finalist)**

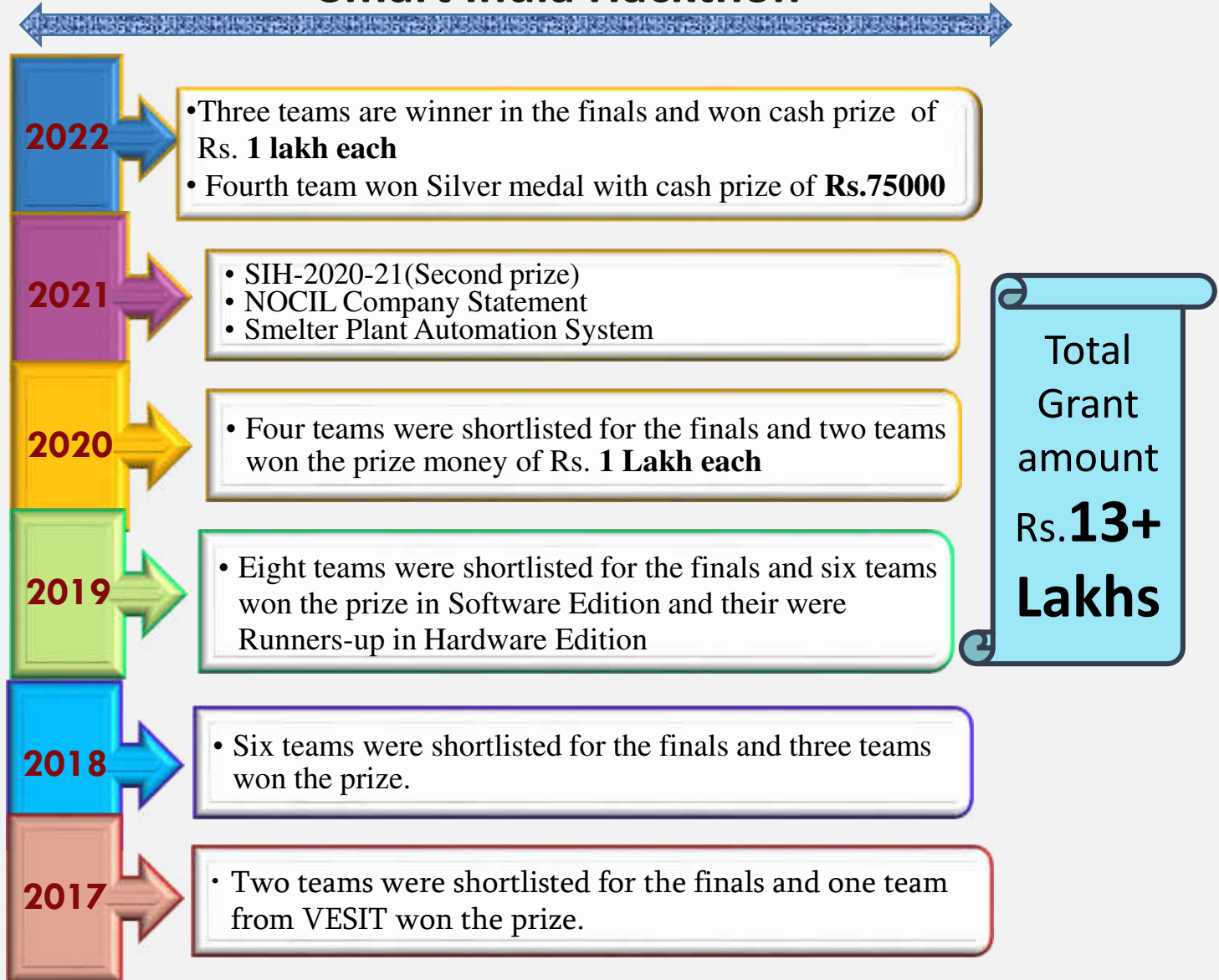
### Projects qualified for the grand finale:

1. Virtual heritage train using simulation that passes through heritage cities and helps to receive Indian Culture Heritage.
2. Diversity in Culinary Practices ( ekyam Bharat).

# Achievements



## Smart India Hackthon



### All Winner Teams SMART INDIA HACKATHON Competition 2022-23



Winner-2022



Winner-2022



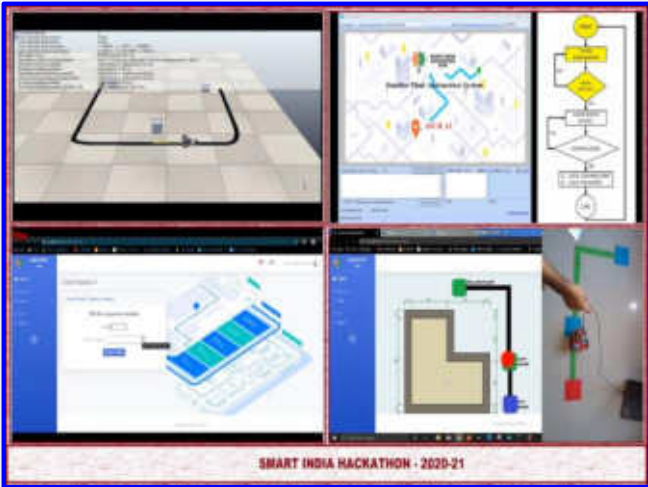
Winner-2022



Silver medal winner

# Achievements

## Smart India Hackthon



SIH-2020-21(Second prize)  
NOCIL Company Statement ,Smelter  
Plant Automation System

Runners-up of 2019 Hardware Edition



Winner - 1st Prize Winner cash prize of Rs. 100000/-  
2018-2019 & 2019-2020  
(Industry Sponsored)



1st runner-up cash prize of 75000/-  
2017-18  
(Ministry of Skill Development & entrepreneurship)



Winners- Persistent Innovation Prize for AICTE  
2017-18

## Achievements

### Swadeshi Microprocessor Challenge-2020



Organized by the MeitY in collaboration with CDAC, IIT Madras, IIT Bombay, and Makervillage.



03 VESIT Product Ideas in 100 Semi Finalists  
Total Award of Rs. 3,00,000/-

01 Team in 30 Finalists  
Award of Rs. 4,00,000/-

Winner of 4<sup>th</sup> Prize  
Award of Rs. 20,00,000/-

## SWADESHI MICROPROCESSOR CHALLENGE

Projects developed using Swadeshi Microprocessor designed in collaboration with CDAC and IIT Madras

VESIT Team developed "**Calibration System for Nuclear Spectroscopy Applications**" and won **4<sup>th</sup> Prize** in the Swadeshi Microprocessor challenge [<https://innovate.mygov.in/swadeshi-microprocessor-challenge/>] organized by the Ministry of Electronics and Information Technology, held in Indian habitat center, New Delhi on 2-3, December 2021.

**The team received the fourth prize award of Rs 20,00,000/-.** (Out of the 30 finalists, 19 were start-ups and 11 from educational institutions)

Team Members: 1. Mrs. Asma Parveen I. Siddavatam (Ph.D. Scholar) 2. Mr. Ajit Tukaram Patil (PG Student) & Dr. P. P Vaidya (Mentor)

**Earlier, as a selection in the 30 finalists, the team had received a cash prize of Rs. 4,00,000/-.**

**Also, the institute received Rs. 3,00,000/- towards the selection of Three (03) VESIT Product Ideas in the Semi-Final Stage (100 Shortlisted Product Ideas in the Minimum Viable Prototypes (MVP) from 6000+ Product Ideas proposed by different educational institutes, start-ups, R&D centres and Industries in India.)**

**Hearty Congratulations to all Members, Faculty & VES Management!!!!!!**

# Achievements

## Deep Blue by Mastek [National level Project competition ]



**2021-22**  
Resume Parser  
[second position]



**2018-19**  
"E- Clean"  
[Best Team]



**2018-19**  
Capacity Planning for  
Public Toilets  
[Finalist]

**2017-18**  
Ministry of Skill  
Development and  
Entrepreneurship  
[1<sup>st</sup> Runner up]

**2017-18**  
[Super Team  
award ]



## Achievements



### Avishkar Research Convention organized by University of Mumbai.



Principal Dr. J. M. Nair and Dr. P. P. Vaidya receiving the Overall Championship (First Rank) award in Engineering & Technology Category in the 13th Intercollegiate/Institute/Department Avishkar Research Convention

Overall Championship (First Rank) award for the college in Engineering and Technology Category 2018-19.



#### A.Y 2021-22

won bronze medal at Level 1: Undergraduate Students (UG) in Engineering and Technology category.

#### A.Y 2020-21

Received SILVER MEDAL in 15<sup>th</sup> Intercollege/ Institute/ Departments Avishkar Research Convention

#### A.Y 2019-20

Prof. Nilima Warke, has secured 3rd rank at University level. Her project was also recognized by the team of Nehru Science Center and this project was selected as "Innovative Idea and Design" which is being considered for incubation in Mumbai University.

Prof. Kanchan Chavan was winner at (Inter-Zonal/Inter-District) round.

#### A.Y 2018-19



Silver medal (Second Prize) in Teachers category at Inter-University State Level competition.

Prof. Ajesh Lekshmi won at the Inter-University level in a Ph.D. category.

Prof. Kadambari Sharma has secured 3rd rank in the Teachers category at the University level.

Participated and presented at Final Round ( University Level)

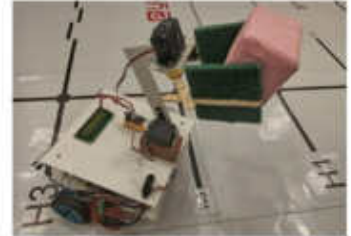


**Achievements**

**E-YANTRA (National Level Competition)**



eYRC 2020 Finalist  
 Among Top 30 Teams



eYIC 2020



**Winners**

<p><b>2019-20</b>                  EYRC: One of the team was finalist among top 30 teams.                  EYIC: Four Teams were Regional Final</p>	<p><b>2018-19</b>                  Two Teams from VESIT were the winners.                  They won the Cash Prize worth Rs. 5000/- each</p>	<p><b>2017-18</b>                  One Team won the Cash Prize worth Rs.20,000 and were the winner among top 18 teams</p>
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e-YIC 2020 National Finalists  
 AIDE: AI and IoT for Disabled and Elderly

e-YIC 2020 Regional Finalists  
 Forest Fire Prediction using ML and IoT Device



# Other Achievements



- 2021:** VCET's National Level Projects
  - AiDD: Assisting ADHD Children
  - sVana 2.0 Bringing Light and Dark Together Hackathon
  - Creative thinking for Creative minds, Rs. 15000
  - Mask detection System, Rs. 11, 000
- 2019:** Buoyancy Innovation, 2019
  - AI for Health care Rs. 20, 000/-
- 2018:** Honeywell Hackathon Challenge
  - 1<sup>st</sup> Runner up, 2018, Rs. 75, 000/-
- 2017:**
  1. Make in India: Hackathon competition
    - held on 29th January 2017 at IIT Mumbai
  2. NAWONMESH - Nationwide Competition
    - organized by AAVRITI, at IIT BOMBAY held on 4th and 5th March 2017.
    - [awarded **1<sup>st</sup>** position].

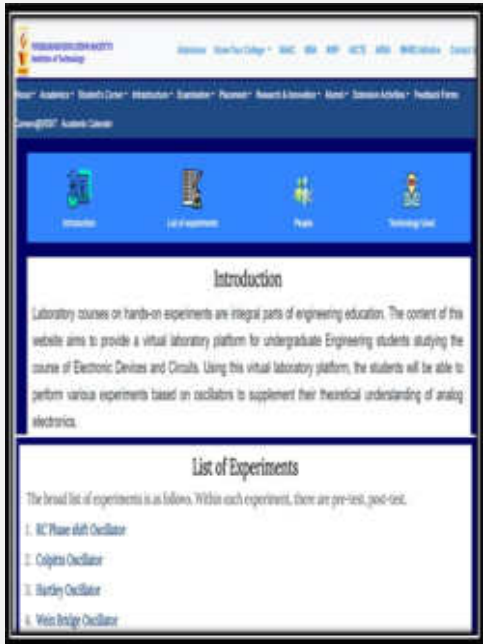
# Virtual Lab



**GOLC Online Lab award certificate for "Optical Fiber Communication Virtual Lab"**



**Optical Fiber Communication Virtual Lab**



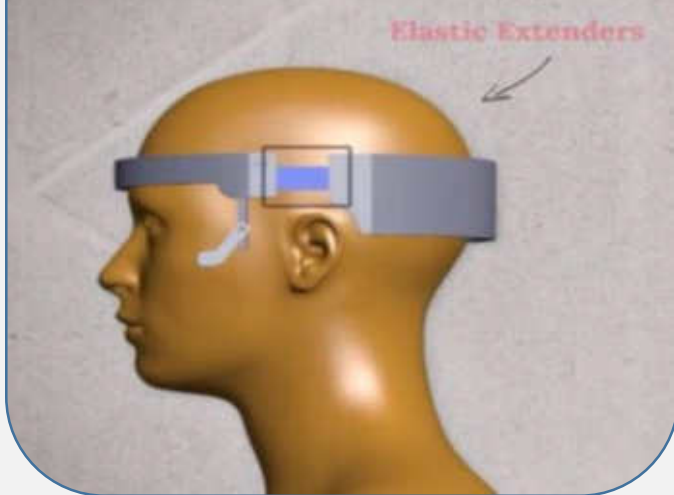
**Electronics Device & Circuits Virtual Lab"**

## Other Achievements

### India Innovation Challenge Design Contest (IICDC)

Organized by **Department of Science and Technology (DST)** and **Texas Instruments (TI)**, anchored by **NSRCEL@IIMB**, powered by AICTE mission and supported by MyGov.

Winners of the Project “**EOG Drowsiness Detection System**”



**More than 18,000 teams had registered**

Two (02) Projects/Teams of VESIT selected in the **Top 60** qualified for the finals and are trained under **NSRCEL@IIMB Launchpad**.

**ISA Scholarship Recipients  
AY:2019**



#### Student Ambassadors

##### Manas Mangaonkar

- GitHub Campus Expert

##### Sujitkumar Singh

- InterviewBit : Campus Ambassador
- DeepLearning.AI : City Ambassador

##### Arnav Bagchi

- Microsoft Learn
- Amazon AWS
- GitHub Campus Expert

#### 2020-21

- AICTE Vishwakarma Award:-Finalist

#### 2019-20

- VJTI Extup,Sankalp Semiconductor Hackathon:- Finalist
- Extravaganza:- 2nd Runner up

## Other Achievements

DR. A P J Abdul Kalam, Teacher's Excellence Award 2020- 21



Dr. (Mrs.) Ranjan Bala Jain  
receiving the award

Certificate of Award



Faculty **Asma Parveen I. Siddavatam** has Bagged **First runner-up** award at Student Research Forum Award for the project title **” Design and Construction of Precision and Sliding Plus Generator “** at **The 35<sup>th</sup> International Conference On VLSI Design and The 21<sup>st</sup> International Conference On Embedded Systems[VLSID2022]**

## Other Achievements

### ASEAN-India Hackathon



#### Blockchain in Logistics.

Yash Mate and his team **won the ASEAN-India 2021 Hackathon** organized by the Ministry of Education in collaboration with the Ministry of External Affairs.



**International Project Competition**  
VISA I 2022, Feb  
Won **1st prize**,  
VEL Tech University,  
Chennai

# Research and Development Ecosystem at VESIT

Recipient of prestigious grants from the BRNS, ISRO, MCGM, AICTE and many.

## 1) Board of Research in Nuclear Sciences (BRNS) Grant

1] Title: Development of Methodology for Indian Evaluated Nuclear Data Library for applications in S & T

- Investigator: Department of Instrumentation Engineering Grant No: 51/14/07/2019-BRNS/36083
- Grant amount: Rs.17,81,276/-

2] Title: Development of an electronic radioactivity simulator

- Investigator: Department of Instrumentation Engineering
- Grant No: 2010/36/41-BRNS/1777
- Grant amount: Rs.21,37,950/-

## 2) Indian Space Research Organization (ISRO) Grant

Title: Martian landform segmentation based on structural similarity

- Investigator: Department of Electronics Engineering
- Grant No: DOS/PAO/GIA/2016-2017/18/374
- Grant amount:Rs. 3,08,000/-

## 3) Star Union Dai-chi Life Insurance Co Ltd. Grant

Title: Swechcha Project-IT solution for maintenance of Community Toilets

- Investigator: Department of Information Technology
- Grant amount: Rs.24,00,000/-

## 4) All India Council for Technical Education (AICTE) Grant

**Under the scheme Modernization and Removal of Obsolescence (MODROB)**

Title: Advanced wireless lab (Software Defined Radio and MIMO Configurations)

- Investigator: Department of Electronics and Telecommunication Engineering
- Grant No: 9/205/IDC/MODROB/Policy-1/2019-20 Grant amount:Rs. 9,11,765/-

## 5) Ministry of Electronics and Information Technology (MEIT) Grant: Swadeshi Microprocessor Challenge

1] Title: Swadeshi Battery Management Systems

- Investigator: Department of Electronics and Telecommunication Engineering
- Grant amount: Rs. 1,00,000/-

2] Title: High-Resolution Multichannel Analyzer for nuclear spectroscopy application

- Investigator: Department of Instrumentation Engineering
- Grant amount: Rs. 1,00,000/-

3] Title: Calibration System for Nuclear Spectroscopy Application

- Grant amount:Rs.25,00,000/-

## Research and Development Ecosystem at VESIT

### 6] Microsoft AI for Earth Grants

(10 Faculty- 47 students) -11 Azure Compute Grant ~\$80000

Investigator: Department of Computer Engineering

Project Titles

- Climate, Air Pollution and Forest Fires : AI based models for Indian case study.
- AI based statistical analysis of pollution due to land and ocean plastic waste in India
- Forecasting of Carbon Emissions due to Meat Industry
- Monitoring and Assessing Coral Reefs in Oceans
- Forecasting of Carbon Emissions due to Meat Industry
- Contagious disease propagation study and containment strategies using Machine Learning : Indian Scenario
- Predicting floods in coastal regions as a direct consequence of deforestation
- Predictive analysis of carbon footprint in metropolitan cities of India.
- Contagious disease propagation study and containment strategies using Machine Learning : Indian Scenario
- Farm Guide
- Rehabilitation and Protection of Coastal Areas
- Water Supply Management and Catchment Control in Drought Prone Regions of Rural India
- Machine Learning based Flood Prediction model for Mumbai, India

### 7) National Grants (48 Faculty, 57 students) Grant ~Rs. 4,13,000

Investigator: Department of Computer Engineering

Project Titles

- AI-based Prediction for Early Detection of Tuberculosis in India based on Environmental Factors
- Sahara: A stanchion for the blind
- Trail-Tracker: Antipoaching Intelligence using AI and IoT
- Coreveillance- Making our world a "SAFER" Place
- Food to People: A Block chain Solution
- FireMan Helper
- Autonomous Luggage carrying Robot using Image Processing
- Transparent and trackable donations using blockchain
- Garuda: The Intelligent Ornithopter
- Smart Locker Cost effective electronic limbs
- Electricity Consumption and Home Automation
- Speed Regulating mechanism for Road Safety (Prof. Richard)
- Home Automation using Panoramic Image
- Location based alert system using Twitter analytics

# Research and Development Ecosystem at VESIT

## STATE OF THE ART RESEARCH AND DEVELOPMENT CELL CATERING TO CUTTING EDGE RESEARCH AND INNOVATION-

- Programmable Reconfigurable Multislope ADC
- High Resolution 32K Nuclear Spectroscopy System

### Research and Development Projects completed with L & T:

- Nanosecond phase shift measurement
- Data Acquisition system for nanosecond phase shift measurement
- Low level signal amplification
- Low level dc restoration system
- Low level signal processing for Rogowski coil

- Research grade Computer Controlled Light Source for Optical/LASER Experiments.
- 1 K Portable Multichannel Analyzer

### Research and Development in going projects:

- Pipeline leakage detection system using optical fiber with L& T
- Partial discharge Analyzer system with IIT Bombay
- Partial discharge characterization system with IIT Bombay
- Covariance analysis of nuclear data uncertainty with BARC
- Geographical Information System(GIS) based application with Persistent System

## Research Collaboration

- Wisconsin School of Business, University of Wisconsin, Madison
- Tata Institute of Fundamental Research, Mumbai
- IIT Mumbai
- Agricultural Research Station, Lonavala
- College of Engineering ,Pune
- Konark IP Dossiers Pvt. Ltd.
- Star unionDalchi
- Union Bank of India
- Larsen & Toubro
- Nair Hospital, Mumbai
- Cerelabs, Mumbai
- Green Works Trust
- Capital City Finance & Investment, Thane
- Pragyan Instruments Pvt.Ltd



Prof. Shashikant Dugad, TIFR , Mumbai with Project Mentors

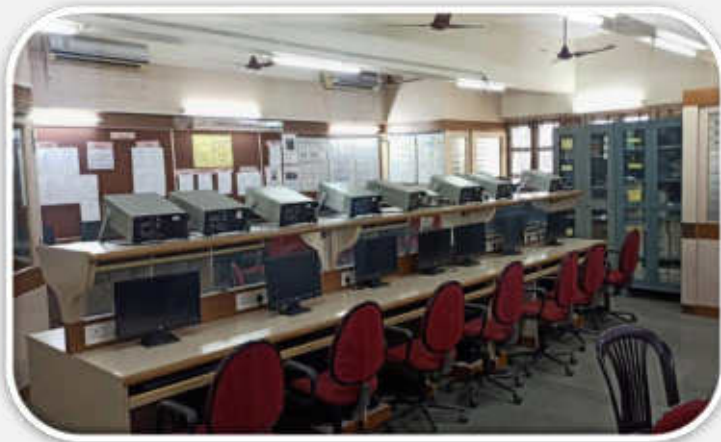
# Department of Electronics & Computer Science

## VISION

To produce globally competent and socially responsible engineers capable of independently solving problems with a sound knowledge of engineering in Electronics and Computer Science.

## MISSION

- To prepare students in the field of engineering in Electronics and Computer Science for lifelong learning.
- To develop professionals who can think critically and provide cost-effective solutions to solve local and global issues using knowledge of Electronics and Computer Science.
- To inculcate a sense of being job providers than job seekers with due consideration to ethical and social values.



## DEPARTMENT HIGHLIGHTS

- UG Programme (B.E.) in Electronics and Computer Science. Established in 2022 with an intake of 60.
- E-Yantra Lab in Collaboration with IITB
- VLSI Lab is equipped with the latest Cadence Software.
- Tinkerer's Lab, E-yantra Lab, Robotics and AI Lab. Industrial Visits and internships for students.
- IIRS – ISRO outreach E-learning Education Programme.
- Student Workshops, Finishing School, Bridge Courses, and Skill Enhancement Lectures (SEL) for students.
- Participation of students in National level competitions: E-yantra and Smart India Hackathon.

## LABORATORY FACILITIES

- Data Science
- Software Engineering
- Computer Programming
- VLSI Design and Communication
- Embedded System
- Robotics and AI
- Electronics Circuit Design & Analysis
- Electronics Workshop – PCB
- Project

# Department of Computer Engineering

## VISION

To create center of excellence in computing by imparting quality education for developing competent professionals.

## MISSION

- To Provide enabling environment through excellence in teaching and Learning to contribute towards industry and society.
- To promote and strengthen interdisciplinary approaches in innovation, creativity and research.
- To facilitate productive employment and higher studies with entrepreneurial attitude and professional ethics.



## DEPARTMENT HIGHLIGHTS

- Equipped with Google Classroom: Powered by Google Workspace, Google meet/NPTEL: Labs, e-Pradarshani, E-Archival & E-Learning Centre: Research & Projects.
- Codecell.
- Participation in various Hackathons, Ideathon and Toycathon, and Various Teaching-Learning processes.
- Experiential Learning: Industry Visits, Industry Internships, VRC Internships, Skill Enhancement Lectures, Workshops, Talks in.
- Participative Learning: Hackathon – Smart India, CIIA, Ideathons ( MHRD). E-Yantra, Institute Innovation Cell in Sustainable Goals Projects, TE and SE Projects mapped to Area of Specialization of Dept. Looking Beyond syllabus – Projects, Research-based Project work in Problem Solving Learning.
- Peer to Peer Learning: Codecell – Tinkerer's of CMPN V-REACH , Prakalp - Pradarshini and Archrivals.
- Industry Interaction:
- Industry Collaboration projects, Industry Visits, Internships, Skill Enhancement Lectures, DAB Member, IAB Members, Project Exhibition – Mentors and Juries, CIIA Projects, Tech4 Good Projects.

## LABORATORY FACILITIES

- Database Management Systems
- Microprocessor and Robotics
- High Performance Computing
- Networking and Mobile Computing
- Cloud Computing
- Object Oriented & Programming
- Project Lab 1 , Project Lab 2
- Machine Learning and Big data Analytics
- Web Engineering and Multimedia (Under AICTE MODROB grant)

## Placement Statistics



## Distinguished Alumni



**Mr. Amit Rambhia**

- 1996
- Chairman, MD & Executive Director.
- PANACHE DIGILIFE LTD. Selected under production-linked incentive (PLI) scheme



**Mr. Neeraj Vashi**  
(Computer-1990-91)  
Founder & Director,  
Nivalink



**Mr. Ajeet Khurana**  
(Computer- 1991)CEO,  
Zebpay, Angel Investor



**Mr. Anubhav Sonthalia**  
(Computer - 2002)  
Co-Founder &  
COO, Sokrati



**Mr. Suraj Nair**  
(Computer-2010)  
Squadron Leader  
"Vayu Sena Award" on  
26th January, 2022



**Ms. Tanushree B.**  
(Computer – 2001)  
CFO & VP-IT, NRB  
Bearings Ltd  
Top 100 CFO in India

# Department of Automation and Robotics

## VISION

To contribute towards creating interdisciplinary engineers in the field of Automation and Robotics who strive towards applying their knowledge for the progress of the society

## MISSION

- To impart knowledge and skills to engineering students to compete at the National and International level in the field of Automation and Robotics.
- To create an environment that promotes research ideas for solving societal problems/issues/challenges.
- To inspire young (budding) engineers to apply knowledge of Automation and Robotics to take up career and work towards making our country self-reliant.



## DEPARTMENT HIGHLIGHTS

- Established in the year 2022-23 with student intake of 60.
- Lab facilities in the department have been developed in consultation with industry experts and so is at par with Industry.
- The Department has a Competency Development Centre for Integrated Automation by Automation Industry Association (AIA) for training students and faculty.
- VESIT-NI LabVIEW Academy in association with National Instruments (USA), is established in the department.
- Highly qualified and experienced faculty
- Good quality projects in collaboration with industry/institutes at Undergraduate level
- Well developed Tinkerer's Lab which is open beyond college hours for providing working space, hardware and training for student projects.
- Support by faculty/alumni to students through mentor-mentee counselling
- Strong Industry-Academia connect for placement, internship and training.

## LABORATORY FACILITIES

- Robotics and Embedded System
- Process Automation
- AIA
- Control Systems
- PLC DCS
- Transducers
- Tinkerer's

# Department of Electronics and Telecommunication Engineering

## VISION

Towards developing a Centre of excellence in the field of Electronics and Telecommunication and to nurture students to become technocrats with a humane outlook.

## MISSION

- To empower the budding engineers to meet the growing challenges of industry.
- To promote a cutting-edge research to benefit the society locally and globally.
- To develop young engineers with human and social intellectual qualities required for practicing as responsible engineers.



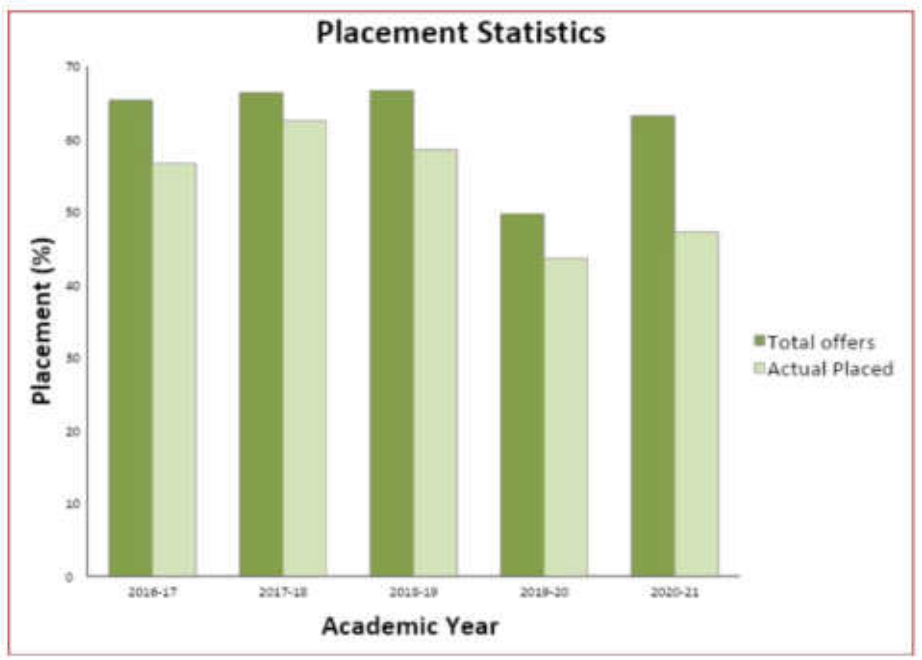
## DEPARTMENT HIGHLIGHTS

- Aptitude training and Preplacement activity, program like GRIN: Get Ready for Industries, Finishing School Activity.
- Industry visit and participating in National Level competition like swadeshi Micro processor challenge, Rediscovering India Virtual Tour App, Toycathon, Smart India Hackthon.
- Vreach-EXTC, SEI, Tinkerer's Lab, Looking Beyond Syllabus.
- Workshops, STTPs/FDPs etc.

## LABORATORY FACILITIES

- TV, VLSI & Satellite Comm.
- Analog & Digital Comm.
- Microprocessor and Robotics
- RF, Microwave & Antenna Comm.
- DSP & IP open source
- DLink Academic, Network & Wireless Comm.
- Optical fiber Comm & Network
- Innovation Lab & Tinkerer's
- PCB, Microprocessor/Microcontroller & Embedded System
- Electronic Devices & Integrated circuits

# Department of Electronics and Telecommunication Engineering



- Highest Salary : 14 LPA (JPMC-CFG)
- Average Salary : 5 LPA

## Distinguished Alumni



**Dilipkumar Khandelwal**  
(EXTC 1998)  
MD and CEO, Deutsche India



**Dr. Siddhartha Raja**  
(EXTC 2002)  
Regional Cluster Lead for South Asia, World Bank Country Office, Kathmandu, Nepal



**Dr. Hemang Shah**  
(EXTC 2002 )  
India Engineering Lead, Qualcomm Technology Licensing, Qualcomm, Bangalore, India



**Pratik Gupta**  
(EXTC 2013)  
Founder, TEKNOLITE, Mumbai, India



**Ganesh Godamgave**  
(EXTC 2018)  
Sub. Lt. Indian Navy, India



**Parikshit Chopra**  
(EXTC 2020)  
Lieutenant, Indian Army, India



# Department of Information Technology

## VISION

To create a high quality academic and research environment, which empowers faculty and students to attain the highest levels of excellence as professionals in the field of Information Technology, in turn leading to the creation of leaders and trend-setters for the next generation IT Industry.

## MISSION

- To Provide quality education that offers opportunities for lifelong learning at multiple academic and professional levels.
- To Promote and support faculty involvement in all aspects of IT research and development.
- To offer state-of-art information technology education, imparting skills for building cutting-edge and innovative IT applications.
- To collaborate with the industry to create new infrastructure, and test beds that would facilitate the IT industry to experiment with new technologies and create next generation products.



## DEPARTMENT HIGHLIGHTS

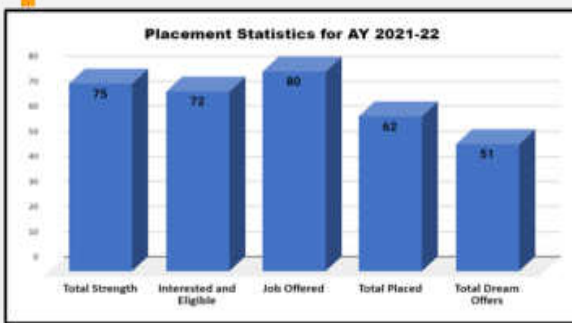
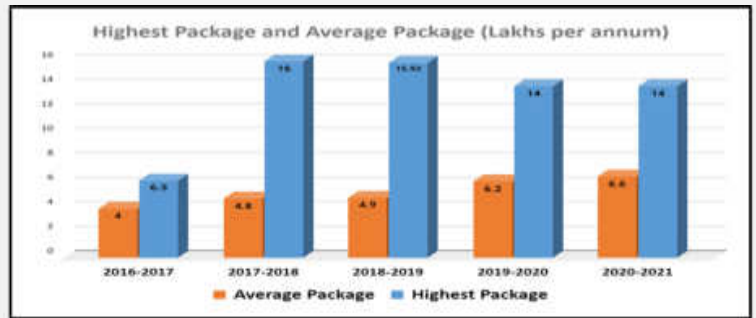
- Participation in National level competitions and Hackthon: स्वदेशी Microprocessor Challenge, Deep Blue, e-Yantra and Smart India Hackthon, Avishkar.
- Workshop and Industrial Visit
- Collaboration - COEP: Industry Collaboration .
- Project Swechcha, Project Ganit – एकलव्य, Institute SUDLife: Project Utkarsh – उत्कर्ष.
- #BridgeTheGap - Finishing school.
- Peer-to-Peer Learning - Dream Students Interaction, #ShareYourExperiences.
- Aptitude and Technical Tests
- AlumniConnect - e-Disha, FDPs/STTP,V-Reach.
- QuestIT,SEL

## LABORATORY FACILITIES

- IOT
- Project
- Big Data Analytics
- Database Management Systems
- Software Engineering and Project Management
- Networking and Security
- Specialized Lab – Cloud Computing (Cloudera Lab)
- Specialized Lab – Machine Learning & Big Data Analytics

# Department of Information Technology

## Placement Statistics



## Distinguished Alumni

 <p><b>Mr Roshan Kulkarni</b> (INFT 2003), Founder and CEO of Mindstix Software labs, Inc</p>	 <p><b>Annirudh Joshi</b> (INFT 2003) CEO of Atreya Innovation Pvt Ltd</p>	 <p><b>Rahul Subramaniam</b> (INFT 2007), Stand up comedian, Synchronised Swimmer</p>	 <p><b>Mr Nikung Gala</b> (INFT 2010), Equity Research Analyst, Principal assets management Ltd</p>	 <p><b>Mr Mayank Khothari</b> (INFT 2010), CEO and founder of ATTO INFOTECH LLP</p>
 <p><b>Dhimant Gandhi</b> (INFT 2010) CEO of QSR Space</p>	 <p><b>Anish Saraf</b> (INFT 2013), Assistant Vice President of JP Morgan Chase, London</p>	 <p><b>CHIRAG TANK</b> (INFT2015), CEO and Founder of DATEN &amp; WISSEN, Product Manager, Technical Trainer</p>	 <p><b>Hitesh Ahuja</b> (INFT 2017), CEO of Algoocean</p>	

# Department of Artificial Intelligence and Data Science

**VISION**

To achieve global standards of excellence in education, innovation and research by using the latest tools in the field of academia and industry serving the greater cause of society.

**MISSION**

- To produce and nurture creative and technically strong engineers in the field of Artificial Intelligence and Data Science.
- To impart quality and value based education leading towards continuous learning and innovation in the field of Artificial Intelligence and Data Science.
- To create an ecosystem which enables in designing and deploying Artificial Intelligence and Data Science based solutions for real world challenges.



**DEPARTMENT HIGHLIGHTS**

- AI warriors club for Departmental Technical Activities.
- “Fundamentals of Deep Learning” NVIDIA DLI certified training for the students as well as teachers of the department.
- Peer to Peer learning.
- Experiential Learning: Industry Internships, VRC Internships, Skill Enhancement Lectures, Workshops.
- Placement Activities for students: Eligibility, Employ ability and Resume Building, Mini-Project: A Steeping Stone to Bright Career.
- Participative Learning: TE and SE Projects mapped to Area of Specialization of Dept.
- Looking Beyond syllabus – Projects
- Research based Project.
- Students and Faculty members involved in Industry projects: SUD Life insurance Project Utkarsh – उत्कर्ष.
- “AI in Agriculture” project by Second Year Students Winner of Looking Beyond Syllabus 2021-22 software track.
- Project by Second Year Students winner of SPIT Strat-a-thon 3.0 - 2021-2022.

<b>LABORATORY FACILITIES</b>	
<input type="checkbox"/>	Artificial Intelligence and Data Analytics
<input type="checkbox"/>	Object Oriented Programming and Data structure
<input type="checkbox"/>	<b>Data Visualization</b>
<input type="checkbox"/>	<b>Natural Language Processing</b>

# Department of Master of Computer Applications

## VISION

Towards leading the way in the area of Computer Application to train and nurture the Students to become successful Technocrats thus benefiting the Society and Nation.

## MISSION

- To provide opportunities to Students so that they understand the Theoretical Principles and apply them.
- To enable the Students to continually master the evolving Tools, Techniques, Trends in Technology and Management and effectively put them in practice in a cost-effective manner.



## DEPARTMENT HIGHLIGHTS

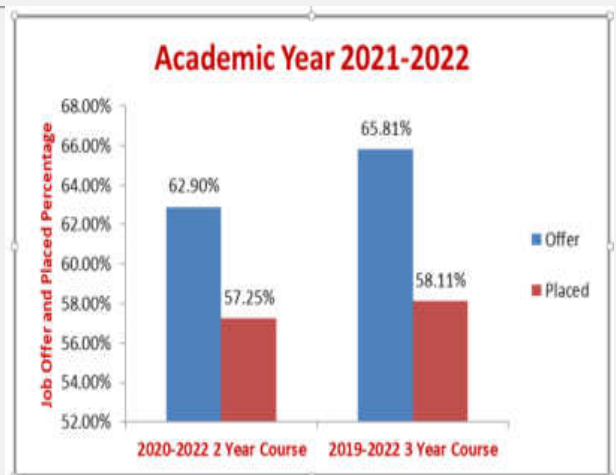
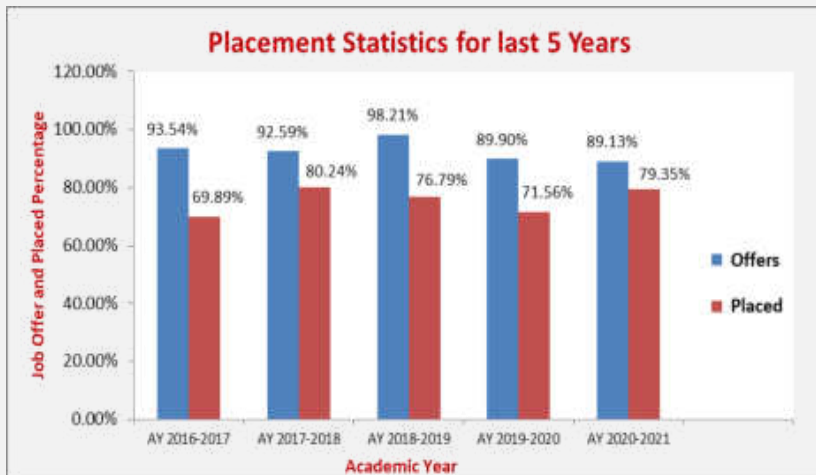
- Students Personality Intellect & Creativity Enhancement (SPICE): program is conducted to support the placement.
- Massive Open Online Courses (MOOC) .
- Institutional Social Responsibility (ISR).
- Skill Enhancement Program (SEP)
- Prakalp Pradarshanam: Project Exhibition Program.
- Industry Internship.
- Mini Projects & Research Paper Publication.
- Institute level competition Looking Beyond Syllabus (LBS) .
- Campus to Corporate .
- VReach: An Alumni Engagement Initiative.

## LABORATORY FACILITIES

- Advanced Database and Advanced Java
- Big Data Analytics and IOT
- Software Testing and Project
- Artificial Intelligence and Machine Learning





# Department of Master of Computer Applications

## Placement Statistics



	2016-17	2017-18	2018-19	2019-20	2020-21	2021-22
<b>Highest Package</b>	16	16	11	14	14	14
<b>Average Package</b>	4.65	4.11	4.6	5.3	5.5	4.5

## Distinguished Alumni

				
<b>Mr. Minhaj Sheikh</b> Batch 2014-2017, Founder and CEO of friendsCampus, Founded in 2017, FriendsCampus is an education startup comprising of a bunch of Educators, entrepreneurs and programmers continually working to tackle the challenge of quality of education	<b>Maunica Shah</b> Batch 2011-2014 Digital Marketeer & Budding Entrepreneur Building Guide2bride - a wedding fashion, photography, decor and gifting Ideas & Inspirations platform with blogs, tutorials, and videos for brides-to-be.	<b>Ankit Goyal</b> Batch 2009-2012 Senior Product Manager - Digital Products at GE Healthcare Specialities: Product Definition and Delivery, Cyber security, DT Audits, Agile, SCRUM, Java, Cloud Foundry, Leading teams, managing project execution from requirements to production.	<b>Ms. Kavita Krishnamurthy</b> Batch 2007-2010 Manager at Accenture Kavita has contribution towards MCA department, as a member of Department Advisory Board, also she has activities like LBS judging, alumni interactions	<b>Mr. Gaurav Salanke</b> Batch 2006-2009, Head-Learning Technology at IMS Learning Resource Pvt Ltd. Gaurav had contributed as an alumni in various activities in VESIT like VREACH Skill enhancement Lectures, Alumni interactions.

## Departments of Electronics Engineering (DSE)

### VISION

To equip our students with the latest technology and implement their ideas to conduct research in selected areas of excellence within traditional disciplines technologically important and relevant to the region and development of the nation.

### MISSION

- The department is committed to produce technocrats ready to handle the state-of-art technologies in the field of Electronics Engineering.
- The department is committed to prepare students to contribute effectively to the profession and society for advanced study, lifelong learning, and conduct research integrated with education in selected areas of excellence.



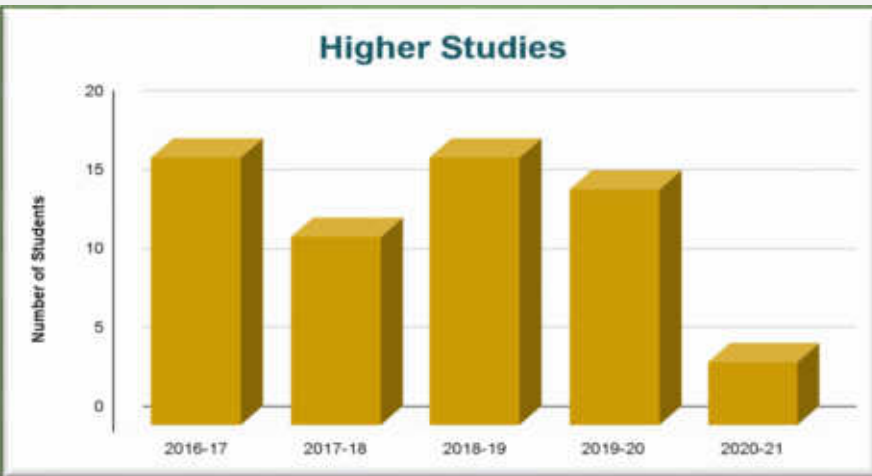
### DEPARTMENT HIGHLIGHTS

- UG Programme (B.E.) in Electronics Engineering. Established in 1984 with an intake of 60.
- NBA re-accredited from 2016 to 2022.
- Excellent track record of placement (70% per year). The highest package offered in 2021-22 is 10+1 LPA.
- E-Yantra Lab in Collaboration with IITB
- VLSI Lab is equipped with the latest Cadence Software
- Consistent record for higher studies abroad (15% per year). State-of-the-Art laboratories and Smart Classrooms.
- Tinkerer's Lab, E-yantra Lab, Robotics and AI Lab. Industrial Visits and internships for students.
- IIRS – ISRO outreach E-learning Education Programme.
- Student Workshops, Finishing School, Bridge Courses, and Skill Enhancement Lectures (SEL) for students.
- Participation of students in National level competitions: E-yantra and Smart India Hackathon

### LABORATORY FACILITIES

- Basic Electrical & Electronics Engineering
- VLSI Design and Communication
- Electronics Circuit Design & Analysis
- Digital Logic Design
- Power Electronics
- Advanced Microprocessor
- IOT
- Project
- Electronics Workshop – PCB
- Robotics and AI

# Departments of Electronics Engineering (DSE)



## Distinguished Alumni



**Ms. Shanti Raghavan**

- 1989 Batch
- Co-founder
- Enable India has received the National Award for the Empowerment of Persons with Disabilities, 2010



**Mr. Saurabh Kanwar**

- 1995 Batch
- Founder & President, Flarepath
- Entrepreneur in the Digital Content domain



**Mr. Saurabh Nanavati**

- 1995 Batch
- CEO,
- Invesco,
- Asset Management Company



**Mr. Dipesh Sutariya**  
(1989 Batch)  
Co-Founder and CEO of Enable India



**Vedant Arvind Rathi**  
(2011 Batch)  
Founder and CEO, WRS Energy Solutions Llp

# Departments of Instrumentation Engineering (DSE)

## VISION

To contribute at National as well as at International level for excellence in technical education and research in field of Instrumentation & Control by creating proactive, noble personalities and integrated individuals who will use the knowledge and be fully dedicated to the progress of society.

## MISSION

- To provide superior education to students to develop critical, analytical and imaginative thinking to enter the workplace with confidence.
- To create an environment for promoting new ideas and research to generate innovators and future leaders.
- To promote academic growth by offering state of art education at undergraduate, post graduate and doctoral level and to pursue lifelong learning so as to lead towards the betterment of society.



## DEPARTMENT HIGHLIGHTS

- First Instrumentation Engineering department established under Mumbai University in 1984.
- Instrumentation department is NBA accreditation for 2 terms of 3 years each.
- Department has a very active Industry interaction for Expert talks, industrial visits, internships and syllabus setting
- Good core placements with more than 25 core companies visiting the department every year.
- Strong Alumni network who are actively involved in career and higher studies guidance.
- Lab facilities at par with Industry
- Good quality research projects undertaken at UG level.

## LABORATORY FACILITIES

- Process Automation
- Transducers & Sensors
- Biomedical /Analytical /Nuclear Instrumentation
- Control Systems & Digital Signal Processing
- Analog & Digital

# Green Campus

## Unnat Bharat Abhiyan



The Mission of Unnat Bharat Abhiyan is to enable participating higher educational institutions to work with the people of rural India in identifying development challenges and evolving appropriate solutions for accelerating sustainable growth.

Under the UBA program, our institute has adopted a cluster of five villages namely as:

**(1) Gargaon (2) Aпти, (3) Asnas, (4) Ambiste Khurd and (5) Amgaon in Wada Taluka, District Thane.**



Rs. 50,000 grant received for installation of Solar Panels



Rs. 50,000 grant received for survey of 5 villages



Rain water Harvesting



Sewage Treatment Plant



Composed Pit



Fuming Hood for Chemistry Lab

# Green Campus



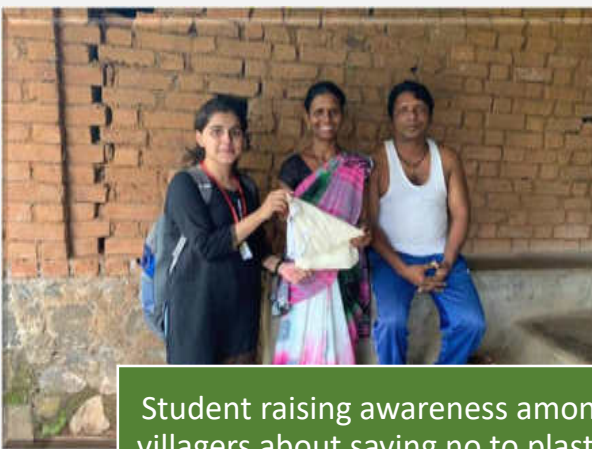
Plastic Elimination Drive



E-Waste collection Management



Tree Plantation



Student raising awareness among villagers about saying no to plastic



Traditional Day celebration



Bliss: Music event



Rangoli Competition

Matrubhasha Divas



*'We need education that forges our life,  
generates the ideal man, imbibes noble thoughts  
and develops the abilities in us.*

*-Swami Vivekanand.*